

# ELCR-1010 Direct Current (DC) Circuits COURSE SYLLABUS Fall Semester 2019

### **COURSE INFORMATION**

Credit Hours/Minutes: 6 Hours / 5250 Minutes Campus / Class Location: Vidalia Campus / Building B, Room 827 Class Meets: Monday through Thursday (MTWR), 9:00 AM to 10:15 AM Course Reference Number (CRN): 20005

### INSTRUCTOR CONTACT INFORMATION

Instructor Name: William (Chip) Greene Office Location: Vidalia Campus / Building B, Room 822 Office Hours: MTWR from 8:00 AM to 9:00 AM and 10:30 AM to 11:30 AM Email Address: wgreene@southeasterntech.edu Phone: (912) 538-3102 Fax Number: (912) 538-3106 Preferred Method of Contact: Text or Email to Instructor

## SOUTHEASTERN TECHNICAL COLLEGE'S (STC) CATALOG AND HANDBOOK

Students are responsible for all policies and procedures and all other information included in Southeastern Technical College's <u>Catalog and Handbook</u> (<u>http://www.southeasterntech.edu/student-affairs/catalog-handbook.php</u>).

### **REQUIRED TEXT**

Electric Circuit Fundamentals, 8<sup>th</sup> edition by Thomas L. Floyd, published by Prentice Hall, ISBN# 0-13-507293-X



### **REQUIRED SUPPLIES & SOFTWARE**

Engineering / Scientific Calculator

Note: Although students can use their smart phones and tablets to access their online course(s), exams, discussions, assignments, and other graded activities should be performed on a personal computer. Neither Blackboard nor Georgia Virtual Technical Connection (GVTC) provide technical support for issues relating to the use of a smart phone or tablet so students are advised to not rely on these devices to take an online course. Students should not share login credentials with others and should change passwords periodically to maintain security.

## **COURSE DESCRIPTION**

This course provides instruction in the theory and practical application of simple and complex direct current circuitry. Topics include laboratory safety practices and procedures, electrical laws and principles, DC test equipment basic series, parallel and combination circuits, complex series and parallel circuits, DC theorems, and applied algebraic concepts.

## **MAJOR COURSE COMPETENCIES / COURSE OUTLINE**

- 1. Laboratory Procedures and Safety Practices
- 2. Electrical Laws and Principles
- 3. DC Test Equipment
- 4. Basic Series, Parallel, and Combination Circuits
- 5. Complex Series and Parallel Circuits
- 6. DC Theorems

## PREREQUISITE(S)

MATH 1012 (out of program), MATH 1013 (diploma), or MATH 1111 (diploma/degree)

## **GENERAL EDUCATION CORE COMPETENCIES**

STC has identified the following general education core competencies that graduates will attain:

- 1. The ability to utilize standard written English.
- 2. The ability to solve practical mathematical problems.
- 3. The ability to read, analyze, and interpret information.

## **STUDENT REQUIREMENTS (TRADITIONAL)**

Students are expected to complete all tests and comprehensive problems by the due dates. A ten point penalty will be assessed for each day a comprehensive problem is late. There are no makeup tests. Tests are made available for several days; therefore, there are no makeup tests. Students who miss a test will be assigned a grade of zero. Students are responsible for policies and procedures included in the STC E-Catalog. All online students must pledge that they have read and understand the STC Online Orientation within the first five days of class. Online students are responsible for checking e-mails and Blackboard announcements DAILY.

### TRADITIONAL ATTENDANCE GUIDELINES

Class attendance is a very important aspect of a student's success. Being absent from class prevents students from receiving the full benefit of a course and also interrupts the learning process. Southeastern Technical College considers both tardiness and leaving early as types of absenteeism. Responsibility for class attendance rests with the student. Regular and punctual attendance at all scheduled classes is required for student success. Students will be expected to complete all work required by the instructor as described in the individual course syllabus.

Instructors have the right to give unannounced quizzes/assignments. Students who miss an unannounced quiz or assignment will receive a grade of 0. Students who stop attending class, but do not formally withdraw, may receive a grade of F and face financial aid repercussions in upcoming semesters.

Instructors are responsible for determining whether missed work may be made up and the content and dates for makeup work is at the discretion of the instructor.

Students will not be withdrawn by an instructor for attendance; however, all instructors will keep records of graded assignments and student participation in course activities. The completion dates of these activities will be used to determine a student's last date of attendance in the event a student withdraws, stops attending, or

receives an F in a course.

## **STUDENTS WITH DISABILITIES**

Students with disabilities who believe that they may need accommodations in this class based on the impact of a disability are encouraged to contact the appropriate campus coordinator to request services.

Swainsboro Campus: Macy Gay, (mgay@southeasterntech.edu), 478-289-2274, Building 1, Room 1210 Vidalia Campus: Helen Thomas, (hthomas@southeasterntech.edu), 912-538-3126, Building A, Room 165

## SPECIFIC ABSENCES

Provisions for Instructional Time missed because of documented absences due to jury duty, military duty, court duty, or required job training will be made at the discretion of the instructor.

### PREGNANCY

Southeastern Technical College does not discriminate on the basis of pregnancy. However, we can offer accommodations to students who are pregnant that need special consideration to successfully complete the course. If you think you will need accommodations due to pregnancy, please make arrangements with the appropriate campus coordinator.

Swainsboro Campus: Macy Gay, (mgay@southeasterntech.edu), 478-289-2274, Building 1, Room 1210 Vidalia Campus: Helen Thomas, (hthomas@southeasterntech.edu), 912-538-3126, Building A, Room 165

It is strongly encouraged that requests for consideration be made **PRIOR** to delivery and early enough in the pregnancy to ensure that all the required documentation is secured before the absence occurs. Requests made after delivery **MAY NOT** be accommodated. The coordinator will contact your instructor to discuss accommodations when all required documentation has been received. The instructor will then discuss a plan with you to make up missed assignments.

### WITHDRAWAL PROCEDURE

Students wishing to officially withdraw from a course(s) or all courses after the drop/add period and prior to the 65% point of the term in which student is enrolled (date will be posted on the school calendar) must speak with a Career Counselor in Student Affairs and complete a Student Withdrawal Form. A grade of "W" (Withdrawn) is assigned for the course(s) when the student completes the withdrawal form.

Important – Student-initiated withdrawals are not allowed after the 65% point. After the 65% point of the term in which the student is enrolled, the student has earned the right to a letter grade and will receive a grade for the course. Please note: Abandoning a course(s) instead of following official withdrawal procedures may result in a grade of "F" (Failing 0-59) being assigned.

Informing your instructor that you will not return to his/her course, does not satisfy the approved withdrawal procedure outlined above.

There is no refund for partial reduction of hours. Withdrawals may affect students' eligibility for financial aid for the current semester and in the future, so a student must also speak with a representative of the Financial Aid Office to determine any financial penalties that may be accessed due to the withdrawal. A grade of "W" will count in attempted hour calculations for the purpose of Financial Aid.

## MAKEUP GUIDELINES (TESTS, QUIZZES, HOMEWORK, PROJECTS, ETC...)

Students are required to take all tests and complete all assignments scheduled during the semester. Failure to take Tests/Exam(s) and complete assignments will result in a grade of zero. There will be no makeup of

assignments or EXAMS. If Internet or browser failure occurs, contact instructor immediately. A decision will be made at that time if the exam will be reset. Instructor reserves the right to deduct points from the exam scores for exceeding the scheduled time limit on the exam and/or requiring student to come to campus to take the final exam. Note: If student notifies instructor about exam problems because of technical issues after the due date or on the last day of the semester, the student will NOT be allowed to make-up the exam. No exceptions! Assignments must be turned in on the assigned date and will not be accepted late, a grade of zero will be given. ALL Assignments are due according to the lesson plan.

## ACADEMIC DISHONESTY POLICY

The STC Academic Dishonesty Policy states All forms of academic dishonesty, including but not limited to cheating on tests, plagiarism, collusion, and falsification of information, will call for discipline. The policy can also be found in the STC Catalog and Student Handbook.

## PROCEDURE FOR ACADEMIC MISCONDUCT

The procedure for dealing with academic misconduct and dishonesty is as follows:

## 1. First Offense

Student will be assigned a grade of "0" for the test or assignment. Instructor keeps a record in course/program files and notes as first offense. The instructor will notify the student's program advisor, academic dean, and the Registrar at the student's home campus. The Registrar will input the incident into Banner for tracking purposes.

## 2. Second Offense

Student is given a grade of "WF" for the course in which offense occurs. The instructor will notify the student's program advisor, academic dean, and the Registrar at the student's home campus indicating a "WF" has been issued as a result of second offense. The Registrar will input the incident into Banner for tracking purposes.

## 3. Third Offense

Student is given a grade of "WF" for the course in which the offense occurs. The instructor will notify the student's program advisor, academic dean, and the Registrar at the student's home campus indicating a "WF" has been issued as a result of third offense. The Vice President for Student Affairs, or designee, will notify the student of suspension from college for a specified period of time. The Registrar will input the incident into Banner for tracking purposes.

## STATEMENT OF NON-DISCRIMINATION

The Technical College System of Georgia (TCSG) and its constituent Technical Colleges do not discriminate on the basis of race, color, creed, national or ethnic origin, gender, religion, disability, age, political affiliation or belief, genetic information, disabled veteran, veteran of the Vietnam Era, spouse of military member, or citizenship status (except in those special circumstances permitted or mandated by law). This nondiscrimination policy encompasses the operation of all technical college-administered programs, federally financed programs, educational programs and activities involving admissions, scholarships and loans, student life, and athletics. It also applies to the recruitment and employment of personnel and contracting for goods and services.

All work and campus environments shall be free from unlawful forms of discrimination, harassment and retaliation as outlined under Title IX of the Educational Amendments of 1972, Title VI and Title VII of the Civil Rights Act of 1964, as amended, the Age Discrimination in Employment Act of 1967, as amended, Executive Order 11246, as amended, the Vietnam Era Veterans Readjustment Act of 1974, as amended, Section 504 of the Rehabilitation Act of 1973, as amended, the Americans With Disabilities Act of 1990, as amended, the

Equal Pay Act, Lilly Ledbetter Fair Pay Act of 2009, the Georgia Fair Employment Act of 1978, as amended, the Immigration Reform and Control Act of 1986, the Genetic Information Nondiscrimination Act of 2008, the Workforce Investment Act of 1998 and other related mandates under TCSG Policy, federal or state statutes.

The Technical College System and Technical Colleges shall promote the realization of equal opportunity through a positive continuing program of specific practices designed to ensure the full realization of equal opportunity.

The following individuals have been designated to handle inquiries regarding the nondiscrimination policies:

American With Disabilities Act (ADA)/Section 504	Title VI - Title IX (Employees) – Equal Employment	
- Equity- Title IX (Students) – Office of Civil Rights	Opportunity Commission (EEOC) Officer	
(OCR) Compliance Officer		
Helen Thomas, Special Needs Specialist	Lanie Jonas, Director of Human Resources	
Vidalia Campus	Vidalia Campus	
3001 East 1 <sup>st</sup> Street, Vidalia	3001 East 1 <sup>st</sup> Street, Vidalia	
Office 165	Office 138B	
Phone: 912-538-3126	Phone: 912-538-3230	
Email: <u>Helen Thomas</u>	Email: Lanie Jonas	
hthomas@southeasterntech.edu	ljonas@southeasterntech.edu	

## ACCESSIBILITY STATEMENT

Southeastern Technical College is committed to making course content accessible to individuals to comply with the requirements of Section 508 of the Rehabilitation Act of Americans with Disabilities Act (ADA). If you find a problem that prevents access, please contact the course instructor.

### **GRIEVANCE PROCEDURES**

Grievance procedures can be found in the Catalog and Handbook located on STC's website.

### ACCESS TO TECHNOLOGY

Students can now access Blackboard, Remote Lab Access, Student Email, Library Databases (Galileo), and BannerWeb via the mySTC portal or by clicking the Current Students link on the <u>Southeastern Technical</u> <u>College (STC) Website (www.southeasterntech.edu</u>).

## **TCSG GUARANTEE/WARRANTY STATEMENT**

The Technical College System of Georgia guarantees employers that graduates of State Technical Colleges shall possess skills and knowledge as prescribed by State Curriculum Standards. Should any graduate employee within two years of graduation be deemed lacking in said skills, that student shall be retrained in any State Technical College at no charge for instructional costs to either the student or the employer.

### **GRADING POLICY**

Assessment/Assignment	Percentage
Exams	30%
Homework	10%
Laboratories	20%
Study Guides	5%
Final Exam*	35%

### **GRADING SCALE**

Letter Grade	Range
А	90-100
В	80-89
С	70-79
D	60-69
F	0-59

#### **Disclaimer Statements**

- (1) Instructor reserves the right to change the syllabus and/or lesson plan as necessary.
- (2) The official copy of the syllabus is located inside the student's online course shell or will be given to them during face to face class time the first day of the semester. The syllabus displayed in advance of the semester in a location other than the course you are enrolled in is for planning purposes only.

### \* ELECTRONICS COMPETENCY EXAMS:

The ELCR-1010 Final Exam is the **ESA Part I – Direct Current Circuits Exam**. The cost for taking this exam is **\$35** payable to the STC Business Office before the last week of the semester. Please plan for this cost to complete the Direct Current series of classes successfully. A grade of 75% or higher on this exam will result in the student being awarded their ESA I certificate from the ISCET.

No minimum grade is required for this exam; however, this exam will carry a **35% grading weight**. Poor performance on this exam could result in a final class grade of <70 out of 100 which will require the student to retake ELCR-1010.

Upon successful completion of all four parts of the ESA exams (i.e. ≥75% on ESA I through IV exams), the student is awarded their Associate CET Certificate from the ISCET.

Students who wish to retake any ESA Exam in order to improve their grades to receive their Associate CET Certificate can do so at a cost of \$15 per exam within two years of the original purchase of their test voucher for that exam.

# **ELCR-1010 Direct Current Circuits**

# Fall Semester 2019 Lesson Plan

## WEEK 1 (AUG IS AUGUST)

Date	Chapter / Lesson	Content	Assignments & Tests Due Dates	Competency Area
<b>Aug</b> 13	None	Class Introduction – Syllabi, Outline, Rules, and Regulations Coverage <b>(Sect is Section)</b>	Read Sections 1.1 & 1.2 [On Blackboard] Read / Review START HERE info POST to appropriate Message Boards	1, b,c
Aug 14	1.1, 1.2	Sect 1.1 – Scientific & Engineering Notation Sect 1.2 – Units & Metric Prefixes	Do Sect 1.1 & 1.2 Reviews Read Sect 1.3 & 1.4	1, a,b,c
<b>Aug</b> 15	1.3	Sect 1.3 – Metric Unit Conversions	Do Sect 1.3 & 1.4 Reviews Read Sect 1.5	1, a,b,c
<b>Aug</b> 19	1.4, 1.5	Sect 1.4 – Measured Numbers Sect 1.5 – Electrical Safety	Watch All Chapter 1 Videos	1, b,c

Date	Chapter / Lesson	Content	Assignments & Tests Due Dates	Competency Area
Aug	1	LabVolt – Trainer Familiarization		1,
20				a,b,c
Aug	1	Chapter 1 Review	Complete Chapter 1 Homework	1,
21			Complete Chapter 1 Study Guides	b,c
Aug	1	Chapter 1 Test	Read Sect 2.1 & 2.2	1,
22				a,b,c
Aug	2.1, 2.2	Sect 2.1 – Atoms	Do Sect 2.1 & 2.2 Reviews	2,7,
26		Sect 2.2 – Electrical Charge	Read Sect 2.3, 2.4 & 2.5	a,b,c
			Watch All Chapter 2 Videos	

## WEEK 3 (SEPT IS SEPTEMBER)

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Aug	2.3 – 2.5	Sect 2.3 – Voltage	Do Sect 2.3, 2.4 & 2.5 Reviews	2,7,
27		Sect 2.4 – Current	Read Sect 2.6 & 2.7	a,b,c
		Sect 2.5 – Resistance		
Aug	2.3 – 2.5	LabVolt – Electronic Quantities		2,3,7,
28				a,b,c
Aug	2.6, 2.7	Sect 2.6 – The Electric Circuit	Do Sect 2.6 & 2.7 Reviews	2,3,7,
29		Sect 2.7 – Basic Circuit Measurements		b,c
Sept		HOLIDAY – LABOR DAY	HOLIDAY – LABOR DAY	
2				
Sept	2.6, 2.7	LabVolt – Switches & Concepts		2,3,7,
3				a,b,c

#### WEEK 4

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Sept	2	Chapter 2 Review	Complete Chapter 2 Homework	2,3,7,
4			Complete Chapter 2 Study Guides	b,c
Sept	2	Chapter 2 Test	Read Sect 3.1 & 3.2	2,3,7,
5				a,b,c
Sept	3.1, 3.2	Sect 3.1 – Ohm's Law	Do Sect 3.1 & 3.2 Reviews	2,3,7,
9		Sect 3.2 – Application of Ohm's Law	Read Sect 3.3 & 3.4	a,b,c
			Watch All Chapter 3 Videos	
Sept	3.3, 3.4	Sect 3.3 – Energy & Power	Do Sect 3.3 & 3.4 Reviews	2,
10		Sect 3.4 – Power in an Electric Circuit	Read Sect 3.5 & 3.6	a,b,c

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Sept	3.1 – 3.4	LabVolt – Ohm's Law		2,3,7,
11				a,b,c
Sept	3.5, 3.6	Sect 3.5 – Power Rating of Resistors	Do Sect 3.5 & 3.6 Reviews	2,
12		Sect 3.6 – Energy Conversion and	Read Sect 3.7 & 3.8	a,b,c
		Voltage Drop in a Resistance		
Sept	3.7, 3.8	Sect 3.7 – Power Supplies	Do Sect 3.7 & 3.8 Reviews	2,3,
16		Sect 3.8 – Introduction to		a,b,c
		Troubleshooting		
Sept	3	Chapter 3 Review	Complete Chapter 3 Homework	2,3,7,
17			Complete Chapter 3 Study Guides	b,c

WEEK 6

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Sept	3	Chapter 3 Test	Read Sect 4.1 & 4.2	2,3,7,
18				a,b,c
Sept	4.1, 4.2	Sect 4.1 – Resistors in Series	Do Sect 4.1 & 4.2 Reviews	2-4,7,
19		Sect 4.2 – Total Series Resistance	Read Sect 4.3, 4.4 & 4.5	a,b,c
Sept	4.3 – 4.5	Sect 4.3 – Current in a Series Circuit	Do Sect 4.3, 4.4 & 4.5 Reviews	2-4,7,
23		Sect 4.4 – Applications of Ohm's Law	Read Sect 4.6 & 4.7	a,b,c
		Sect 4.5 – Voltage Sources in Series	Watch Chapter 4 Video	
Sept	4.6, 4.7	Sect 4.6 – Kirchoff's Voltage Law	Do Sect 4.6 & 4.7 Reviews	2,4,7,
24		Sect 4.7 – Voltage Dividers	Read Sect 4.8, 4.9 & 4.10	a,b,c

# WEEK 7 (OCT IS OCTOBER)

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Sept	4.8 –	Sect 4.8 – Power in Series Circuits	Do Sect 4.8, 4.9 & 4.10 Reviews	2,4,7,
25	4.10	Sect 4.9 – Voltage Measurements		a,b,c
		Sect 4.10 – Troubleshooting		
Sept	4	LabVolt – Series Resistive Circuits		3,4,
26				a,b,c
Sept	4	Chapter 4 Review	Complete Chapter 4 Homework	4,
30			Complete Chapter 4 Study Guides	a,b,c
Oct	4	Chapter 4 Test	Read Sect 5.1 & 5.2	4,
1				a,b,c

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Oct	5.1, 5.2	Sect 5.1 – Resistors in Parallel	Do Sect 5.1 & 5.2 Reviews	4,
2		Sect 5.2 – Total Parallel Resistance	Read Sect 5.3 & 5.4	a,b,c
Oct	5.3 <i>,</i> 5.4	Sect 5.3 – Voltage in a Parallel Circuit	Do Sect 5.3 & 5.4 Reviews	4,
3		Sect 5.4 – Application of Ohm's Law	Read Sect 5.5 & 5.6	a,b,c
Oct	5.1 - 5.4	LabVolt – Parallel Resistive Circuits	Watch Chapter 5 Video	3,4,
7				a,b,c
Oct	5.5 <i>,</i> 5.6	Sect 5.5 – Kirchoff's Current Law	Do Sect 5.5 & 5.6 Reviews	4,
8		Sect 5.6 – Current Dividers	Read Sect 5.7 & 5.8	a,b,c
		MID-TERM (for Full Term)		

WEEK 9

Date	Chapter / Lesson	Content	Assignments & Tests Due Dates	Competency Area
<b>Oct</b> 9	5.7, 5.8	Sect 5.7 – Power in Parallel Circuits Sect 5.8 – Troubleshooting	Do Sect 5.7 & 5.8 Reviews	4, b,c
<b>Oct</b> 10	No Class	Staff Development Day – No Class	Staff Development Day – No Class	No Class
<b>Oct</b> 14	5	Chapter 5 Review	Complete Chapter 5 Homework Complete Chapter 5 Study Guides	4, b,c
<b>Oct</b> 15	5	Chapter 5 Test	Read Sect 6.1 & 6.2	4, a,b,c
<b>Oct</b> 16	6.1, 6.2	Sect 6.1 – Identifying Series-Parallel Sect 6.2 – Analysis of Series-Parallel	Do Sect 6.1 & 6.2 Reviews Read Sect 6.3 & 6.4	4, b,c

## **WEEK 10**

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Oct	6.3 <i>,</i> 6.4	Sect 6.3 – Voltage Dividers with Loads	Do Sect 6.3 & 6.4 Reviews	4,
17		Sect 6.4 – Loading Effect of Voltmeters		b,c
Oct	6.1 - 6.4	LabVolt – Series/Parallel Resistive	Watch Chapter 6 – Part 1 Video	3,4,
21		Circuit		a,b,c
Oct	6.1 - 6.4	Chapter 6: Series-Parallel Circuits -	Read Sect 6.5	4,
22		Sect 6.1 - 6.4 Test		a,b,c
Oct	6.5	Sect 6.5 – The Wheatstone Bridge	65% Point for Full Term Fall	5,
23			Semester	b,c

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Oct	6.5	Sect 6.5 – The Wheatstone Bridge	Watch Chapter 6 – Part 2 Video –	5,7,
24			Wheatstone Bridge	b,c
Oct	6.5	Sect 6.5 – The Wheatstone Bridge	Read Sect 6.6	5,7,
28			Do Sect 6.5 Review	a,b,c
Oct	6.6	Sect 6.6 – Thevenin's Theorem	Watch Chapter 6 – Part 2 Video –	6,7,
29			Thevenin Equivalent Circuit	b,c
Oct	6.6	Sect 6.6 – Thevenin's Theorem	Do Sect 6.6 Review	6,7,
30				b,c

## WEEK 12 (NOV IS NOVEMBER)

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Oct	6.6	LabVolt – Thevenin Circuits	Read Sect 6.7	3,6,
31				a,b,c
Nov	6.7	Sect 6.7 – Maximum Power Transfer		6,
4				b,c
Nov	6.7	Sect 6.7 – Maximum Power Transfer	Do Sect 6.7 Review	6,
5				b,c
Nov	6.5 – 6.7	Chapter 6: Series-Parallel Circuits -	Read Sect 6.8	6,
6		Sect 6.5 - 6.7 Test		a,b,c

#### **WEEK 13**

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Nov	6.8	Sect 6.8 – The Superposition Theorem	Watch Chapter 6 – Part 2 Video –	3,6,7,
7			Superposition Theorem	a,c
Nov	6.8	Sect 6.8 – The Superposition Theorem	Do Sect 6.8 Review	3,6,7,
11			Read Sect 6.9	a,b,c
Nov	6.9	Sect 6.9 – Troubleshooting	Do Sect 6.9 Review	3,
12			Complete Chapter 6 Homework	a,b,c
			Complete Chapter 6 Study Guides	
Nov	6.8	Chapter 6: Series-Parallel Circuits -	Read Appendix C	3,
13	6.9	Sect 6.8 - 6.9 Test		a,c

# WEEK 14 (APP IS APPENDIX)

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Nov	Арр С	Current Sources		2,
14				b,c
Nov	Арр С	Norton's Theorem		6,7,
18				b,c
Nov	Арр С	Millman's Theorem		6,7,
19				b,c
Nov	Арр С	LabVolt – Millman's Theorem		3,6,
20				a,b,c

#### WEEK 15 (DEC IS DECEMBER)

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Nov	Арр С	Appendix C Test		6,7,
21				b,c,d
Nov	1-6	ISCET ESA Exam Review	Study for Final Exam	1-7,
25				b,c
Nov	1-6	ISCET ESA Exam Review	Study for Final Exam	1-7,
26				b,c
Nov		HOLIDAY - THANKSGIVING	HOLIDAY - THANKSGIVING	
27				
Nov		HOLIDAY - THANKSGIVING	HOLIDAY - THANKSGIVING	
28				
Dec	1-6	ISCET ESA Exam Review	Study for Final Exam	1-7,
2		Semester Classes End		b,c

#### FINAL EXAM WEEK

Date	Chapter	Content	Assignments & Tests Due Dates	Competency
	/ Lesson			Area
Dec	1-6	DC Circuits Final Exam [Proctored]		1-7,
3		ISCET ESA-1 Exam		a,b,c
Dec	1-6	DC Circuits Final Exam [Proctored]		1-7,
4		ISCET ESA-1 Exam		a,b,c

### **COMPETENCY AREAS:**

- 1. Laboratory Procedures and Safety Practices
- 2. Electrical Laws and Principles
- 3. DC Test Equipment
- 4. Basic Series, Parallel, and Combination Circuits
- 5. Complex Series and Parallel Circuits
- 6. DC Theorems
- 7. Applied Algebraic Concepts

### **GENERAL CORE EDUCATIONAL COMPETENCIES:**

- a) The ability to utilize standard written English.
- b) The ability to solve practical mathematical problems.
- c) The ability to read, analyze, and interpret information.